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APPLICATION FOR U.S. PATENT  
TRANSMITTAL FORM

THE COMMISSIONER OF PATENTS  
AND TRADEMARKS  
Washington, D.C. 20231

Docket No. \_\_\_\_\_-CS

Sir:

Transmitted herewith for filing is the patent application of:

Inventor(s): MELANSON, John Laurence

For: CIRCUITS AND METHODS FOR REDUCING INTERFERENCE  
FROM SWITCHED MODE CIRCUITS

Enclosed are: 3 Sheets of Drawings


An assignment of the invention to:

LARGE ENTITY FEE CALCULATION					FEE
	Number		Number Extra	Rate	Basic Fee \$ 690.00
Total Claims	25	- 20 =	5	x \$18=	\$ 90.00
Independent Claims:	4	- 3 =	1	x \$78=	\$ 78.00
Multiple Dependent Claims:	N/A	N/A	N/A	N/A	\$ N/A
Total Filing Fee =					\$858.00

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Please return the original Assignment document to the undersigned attorney for Applicant following recordal of same and address it to: WINSTEAD SECHREST & MINICK P.C., P. O. Box 50784, 1201 Elm Street, Dallas, Texas 75270.

Date August 30, 2000

  
James J. Murphy  
Attorney for Applicant  
Registration No. 34,503

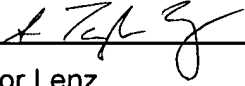
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Date of Deposit August 30 2000

I hereby certify that this 19 pages of Application, 3 pages of Drawings, fully executed Declaration and Power of Attorney, fully executed Assignment, Assignment Cover Sheet and Assignment Recordal Fee in the amount of \$40.00, Patent Filing Fee Transmittal sheet and Filing Fee check in the amount of \$858.00 application fees, are being deposited with sufficient postage with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.

  
S. Taylor Lenz

Signed August 30 2000

CIRCUITS AND METHODS FOR REDUCING INTERFERENCE  
FROM SWITCHED MODE CIRCUITS

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